

AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph beginning on page 1 at line 23 with the following amended paragraph:

Moreover, the instruction sets provided in the three classes of processors currently used in network applications—RISC-based processors, augmented RISC processors, and network specific processors—are insufficiently tailored to packet modification operations to allow ready attainment of the required packet throughputs of 10 GB/s or more. Either the instruction sets of these processors do not are too simple to allow packet modification operations to be efficiently performed, or they have been augmented through “hard-coded” operations that which are insufficiently flexible to handle the diverse types of packet modification operations required.

Please delete the section beginning on page 5 at line 8 entitled “Related Applications,” and replace it with the following replacement section:

RELATED APPLICATIONS

The following applications are commonly owned by the assignee hereof, are being filed on even date herewith, and are each incorporated by reference herein as though set forth in full:

Howrey Dkt. No.	Extreme Dkt. No.	Title
02453.0025.NPUS00	P111	PACKET PROCESSING SYSTEM ARCHITECTURE AND METHOD
02453.0025.NPUS01	P153	PACKET PROCESSING SYSTEM ARCHITECTURE AND METHOD
02453.0026.NPUS00	P122	PACKET DATA

MODIFICATION
PROCESSOR

02453.0027.NPUS00	P124	SYSTEM AND METHOD FOR PACKET PROCESSOR STATUS MONITORING
02453.0028.NPUS00	P126	METHOD AND SYSTEM FOR INCREMENTALLY UPDATING A CHECKSUM IN A NETWORK DATA PACKET
02453.0029.NPUS00	P127	SYSTEM AND METHOD FOR EGRESS PACKET MARKING
02453.0030.NPUS00	P128	SYSTEM AND METHOD FOR ASSEMBLING A DATA PACKET
02453.0033.NPUS00	P123	DATA STRUCTURES FOR SUPPORTING PACKET DATA MODIFICATION OPERATIONS
<u>02453.0043.PZUS00</u>	<u>P156</u>	<u>RECEIVE-SIDE PACKET PROCESSING SYSTEM</u>